

**Notice of References Cited**

Application/Control No.

09/886,167

Applicant(s)/Patent Under  
Reexamination  
POFF ET AL.

Examiner

Uyen T. Le

Art Unit

2171

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Abdelguerfi et al, "A hardware accelerator for smart information systems", IEEE 1993, pages 323-327.
	V	Skorc et al, "Architectural requirements for multimedia image compression and a solution based on VLSI hardware accelerator", IEEE 1994, pages 312-320.
	W	Tomiyasu et al, "KUMP/D: the Kyushu university multi-media processor", IEEE 1995, pages 367-374.
	X	S. Kang, "High performance hardware accelerator for design-error simulation", IEE Proc. Circuit Devices Syst. Vol. 144, No. 2, April 1997, pages 81-87.

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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